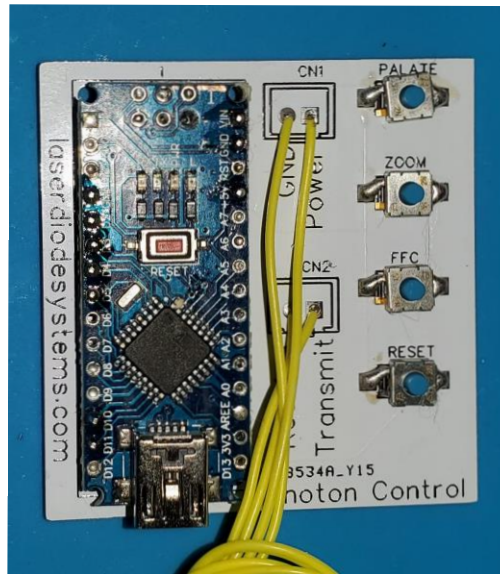


LDS TAU(2) Control Board – Connection Matrix PCB V1

5-6VDC Positive (share wth Core)
Receive (cam Tx)
Transmit (cam Rx)

****See Photon Connections on
RIGHT****

**The Power Ground is
ALSO for the RS232
Ground.**



Power Camera & Board from same,
regulated power source 5-6vdc
5V Nominal. DO NOT EXCEED 6V.

This way you share common ground
with Control Board & Camera.

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PC Rev 4228.V1
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Table 1: Primary I/O Connector Pin Definition

Pin #	Signal Name	Pin #	Signal Name
1	RS232 TX (see 3.1.7)	2	RS232 RX (see 3.1.7)
3	XP D17	4	XP D16
5	DGND	6	DGND
7	Reserved	8	Reserved
9	LVDS CLK P (see 3.1.4.3)	10	LVDS CLK N (see 3.1.4.3)
11	LVDS SYNC P (see 3.1.4.3)	12	LVDS SYNC N (see 3.1.4.3)
13	LVDS DATA1 P (see 3.1.4.3)	14	LVDS DATA1 N (see 3.1.4.3)
15	LVDS DATA2 P (see 3.1.4.3)	16	LVDS DATA2 N (see 3.1.4.3)
17	DGND	18	DGND
19	XP D15	20	XP D14
21	XP D13	22	XP D12
23	XP D11	24	XP D10
25	XP D9	26	XP D8
27	DGND	28	DGND
29	XP D7	30	XP D6
31	XP D5	32	XP D4
33	XP D3	34	XP D2
35	XP D1	36	XP D0
37	DGND	38	DGND
39	XP CLK OUT	40	Reserved
41	DGND	42	DGND
43	VIDEO H (see 3.1.3)	44	VIDEO L (see 3.1.3)
45	DGND	46	3V3 OUT (see 3.1.2)
47	MAIN PWR RTN (see 3.1.2)	48	MAIN PWR (see 3.1.2)
49	MAIN PWR RTN (see 3.1.2)	50	MAIN PWR (see 3.1.2)

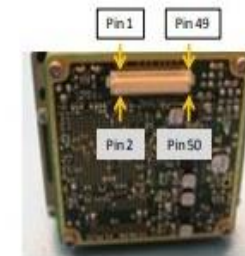


Figure 1: Primary I/O Connector Pinout, Hirose #DF12-50DS-0.5V(86)